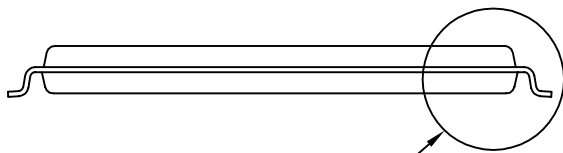
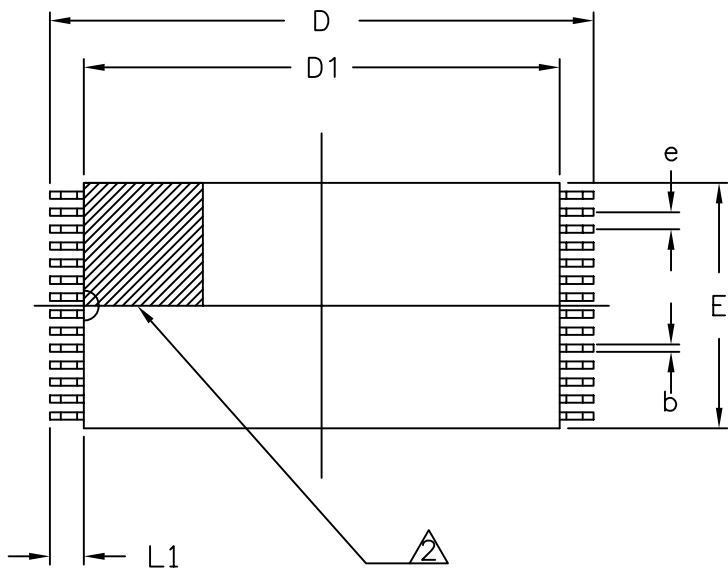


REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	NEW DRAWING		
B	PER ECN # 41195	7/04	

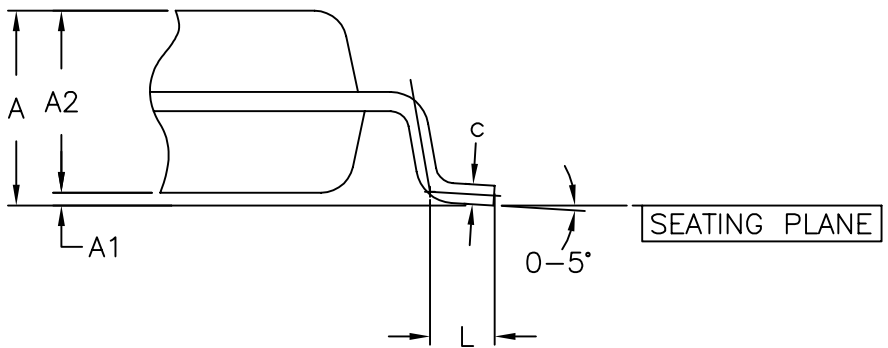


SEE DETAIL A


NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS
2. DETAILS OF PIN 1 IDENTIFIER ARE OPTIONAL BUT ONE HALF OF ITS AREA MUST BE LOCATED WITHIN THE ZONE INDICATED.
3. LEADS SHALL BE COPLANER WITHIN 0.1mm
4. MEETS JEDEC MO-183

DIM	MIN	MAX
A	-	1.20
A1	0.05	0.15
A2	0.90	1.05
b	0.07	0.21
c	0.10	0.20
D	13.30	13.55
D1	11.70	11.90
E	7.90	8.10
e	0.55 BSC	
L	0.30	0.70
L1	0.80 BSC	



DETAIL A

SIGNATURE		DATE	 <b>Dallas Semiconductor</b> TITLE MARKETING OUTLINE 28 LD. TSOP(1), 8X13.4 MM			
DOC. CONTROL:						
ENGR. MGR:						
MFG. ENGR:						
CHECKED BY:	TM	7/04	SIZE	FSCM NO	PART NO.	REV
DRAWN BY:	JFD	7/04	A		56-G5003-000	B
DO NOT SCALE DWG.			SCALE N/A		SHEET 1 OF 1	