



DS21348/Q348

3.3V T1/E1/J1 Line Interface Unit Single/Quad

REVISION B1 ERRATA

The errata listed below describe situations where DS21348/Q348 revision B1 components perform differently than expected or differently than described in the data sheet. Dallas Semiconductor/Maxim intends to fix these errata in subsequent die revisions.

This errata sheet only applies to DS21348 and DS21Q348 revision B1 components. Revision B1 components are branded on the topside of the package with a six-digit code of the form yywwB1, where yy and ww are two-digit numbers representing the year and work-week of manufacture, respectively. To obtain an errata sheet on another DS21348/Q348 die revision, see the Maxim website at www.maxim-ic.com.

1. TRANSMIT OPEN CIRCUIT DETECTION

Description

The Transmit Open Circuit Detect bit (TOCD) should be set when the device detects an open circuit at TTIP and TRING. However, the performance of the bit (SR.1) is unreliable. It is especially unreliable in sparse data patterns where no B8ZS is used.

2. RECEIVE LEVEL INDICATION

Description

The DS21348/Q348 should report the signal strength in RTIP and RRING in 2.5 dB increments via RL3-RL0 (RIR2.7 – RIR2.4) bits in Receive Information Register 2. However, there is an error of ± 2 LSBs i.e., ± 5 dB in the receive level indication.

3. MARGINAL PULSE TEMPLATE

Description

Due to slow slew rates, the pulse template is marginal for the following line build outs for T1 (ETS = 1):

L2	L1	L0	V _{DD}	Application	N	Return Loss	Rt
0	0	1	3.3V	DSX-1 (133 to 266 feet)	1:2	NM	0Ω
0	1	0	3.3V	DSX-1 (266 to 399 feet)	1:2	NM	0Ω
0	1	1	3.3V	DSX-1 (399 to 533 feet)	1:2	NM	0Ω
1	0	0	3.3V	DSX-1 (533 to 655 feet)	1:2	NM	0Ω