



APPLICATION NOTE 3919

Initialization and Configuration of the DS26303 LIU

Abstract: This application note describes how to properly configure the DS26303 T1, E1, J1 Line Interface Unit (LIU). It contains C style example code and will ease the initial software development by allowing the designer to quickly achieve basic system operation.

Introduction

When developing software for a newly designed telecommunications system, the task of achieving basic device operation is often the toughest undertaking. The [DS26303](#) transceiver adds the complexity of an extensive set of functions and multiport operation. To ease the initial hurdle to getting the system up and running, Maxim created a C code style example which will initialize the devices for basic operation in either T1 or E1 mode. The software developer only has to modify the code for the desired operation and write code for two system-dependant functions. Once the code is compiled it should be ready to load onto the system for test and evaluation. It should be noted that there are two variants of the DS26303, these are the DS26303-120 and the DS26303-75. While the differences are very minor and only affect a couple of settings, one needs to be aware of which device is used in a design.

Code Example

The following code example in **Figure 1** will need some modification before it can be correctly compiled for use in the target system. The code for the function calls "write(address, data)" and "wait(millisecods)" are system-dependant, so they need to be written for the current microprocessor environment. The code assumes that the device is mapped into a 16-bit local bus at address offset 0x0000 and that the device data bus is only eight bits. If this is not the case, either the code can be modified or the function calls can be written to account for this. The code also contains may different settings for certain registers to give the developer several options for items like clock frequency, line coding, and more. Although the code covers a wide range of basic functionality, it is in no way complete. The data sheet should be referenced for any additional desired functionality.

```

/*
 * Configuration Example For DS26303-120 running in E1 Mode.
 *
 * This Example assumes E1 operation as the function call for T1/J1 configuration
 * has been commented out. Simply comment out the E1 configuration function
 * call and uncomment the T1 configuration function call for E1 operation. An
 * individual function call for J1 operation is not present because it is very
 * similar to T1 operation and could easily be implemented in that function.
 *
 * This file follows C style conventions. However actual code for the function
 * calls listed below are implementation specific and need to be added.
 *
 * Function Calls: write(address, data), wait(millisecounds)
 *
 * The following comments only indicate some of the possible clock sources
 * that can be used for either E1 or T1/J1 operation.
 *
 * Master clock configuration can use multiples of n = 1, 2, 4, or 8
 * MCLK = can be a n x 1.544 or n x 1.544 MHz signal for E1 or T1/J1 Operation
 *
 * TCCLK = Must be a 2.048 MHz signal for E1 Operation
 * TCCLK = Must be a 1.536 MHz signal for T1/J1 Operation
 */
void initialization_setup()
{
    /* Global Initialization Setup */

    /* Reset all channels to their default values */
    write(0x001F, 0x00); /* ADDR, Set Address Pointer to Primary Register Bank
    write(0x000A, 0x00); /* 000, Reset All Channels

    /* Wait 1 ms for reset to complete */
    wait (1);

    /* The Master Clock Select Register is important for proper */
    /* device operation consult the data sheet for all possible configurations. */
    write(0x001F, 0x01); /* ADDR, Set Address Pointer to Individual LDD Register Bank
    write(0x0004, 0x00); /* MC, E1 Mode Only MCLK=1.544, TCCLK & CLKM Disabled
    /* write(0x0004, 0x00); /* MC, T1/J1 Mode Only MCLK=1.544, TCCLK & CLKM Disabled

    /* write(0x0004, 0x01); /* MC, T1/J1 or E1 Mode MCLK=3.088, TCCLK & CLKM Disabled
    /* write(0x0004, 0x02); /* MC, T1/J1 or E1 Mode MCLK=6.176, TCCLK & CLKM Disabled
    /* write(0x0004, 0x03); /* MC, T1/J1 or E1 Mode MCLK=12.352, TCCLK & CLKM Disabled

    /* write(0x0004, 0x04); /* MC, T1/J1 or E1 Mode MCLK=2.048, TCCLK & CLKM Disabled
    /* write(0x0004, 0x05); /* MC, T1/J1 or E1 Mode MCLK=4.096, TCCLK & CLKM Disabled
    /* write(0x0004, 0x06); /* MC, T1/J1 or E1 Mode MCLK=8.192, TCCLK & CLKM Disabled
    /* write(0x0004, 0x07); /* MC, T1/J1 or E1 Mode MCLK=16.384, TCCLK & CLKM Disabled

    /* Wait 1 ms for clock to settle after configuration */
    wait (1);

    /* The OC Register is able to globally control the ASD During LOS, Short Circuit */
    /* Protection, Line Coding, SN Depth, SN Function, and JA Enable Functions */
    write(0x001F, 0x00); /* ADDR, Set Address Pointer to Primary Register Bank
    write(0x000F, 0x00); /* OC, Enable or Internal Impedance, Disable Global Controls

    /* Global Initialization Complete */

    /* Configuration Begin */
    e1_configure();
}

```

[Download complete code](#) (TXT, 16kB)

Figure 1. Code to initialize and configure the DS26303 line interface unit.

References If you have additional questions on the LIU initialization and configuration, please contact the Telecommunication Applications support team by email, telecom.support@dalsemi.com, or telephone at 01-972-371-6555.

For more information about the DS26303 Octal T1/E1/J1 Line Interface Unit please consult the appropriate data sheet which is available on the Maxim website at [T/E Carrier and Packetized Products](#).

Application Note 3919: www.maxim-ic.com/an3919

More Information

For technical support: www.maxim-ic.com/support

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